

三相脉宽调制整流器的约束预测控制

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摘要: 本文将基于并行神经网络优化的约束模型预测控制(MPC)应用于脉宽调制(PWM)整流器中, 提高了电网的质量. 在三相静止坐标系下, 建立了三相PWM整流器的解耦数学模型, 采用约束模型预测控制策略, 突破了有限集和无约束条件下预测控制的局限性. 为了提高单步优化的速度, 采用神经网络优化算法求解模型预测控制的在线优化. 在保证系统单位功率因数的前提下, 当系统负载突然变化时, 具有快速动态响应稳定输出直流电压的性能. 采用FPGA控制器实现并行计算, 减少了预测控制算法的计算时间. 最后, 通过仿真和实验结果得到, 采用本文的控制策略, 总谐波失真(THD)降低了2.5%, 达到稳态的时间大约是PI控制算法的五分之一, 为12 ms, 验证了该方法的可行性和有效性.

关键词: 三相脉宽调制整流器; 模型预测控制; 单位功率因数; 神经网络优化

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Constrained model predictive control for a three-phase pulse width modulation rectifier

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Abstract: In this paper, constrained model predictive control (MPC), based on parallel neural network optimization, is proposed to apply to pulse width modulation (PWM) rectifier and improve power quality. A decoupled model of three-phase rectifier under three-phase stationary coordinates system is built. Then, this constrained MPC method breaks the limits of predictive control with finite set and without constraints. Neural network optimization is used to solve online optimization of MPC and accelerate single step optimization. This method is proposed to guarantee unity power factor, which also provides a regulated output DC-voltage with fast dynamics response against sudden changes in the load. A FPGA controller is employed to implement parallel calculation so as to reduce the computation time on predictive control algorithm. Simulation and experimental results are compared to verify effectiveness and feasibility of the proposed method. The measured total harmonic distortion (THD) is 2.5% lower and the time to reach steady state is 12ms, which is almost one fifth of PI control.

Key words: three-phase PWM rectifier; model predictive control; unity power factor; neural network optimization

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1 Introduction

Along with shortage of petroleum energy and serious environmental pollution problems, electric vehicles (EVs) and plug-in hybrid electric vehicles (PHEVs), as a low-carbon way to travel, has drawn the attention of researches and customers. However, massive harmonics is brought to the power grid by EVs and their chargers. The three-phase pulse width modulation (PWM) rectifier, also referred to as AC/DC converter belonging to charge equipment, has been successfully applied to guarantee unity power factor^[1-2].

Many control strategies have been investigated for three-phase PWM rectifier. The controller with the characteristic of fast dynamic response and high control

precision, is essential to improve power quality and decrease the harmonics interference. The decoupling control of active power and reactive power of three-phase PWM rectifier is realized with double closed-loop control structure^[3]. An appropriate controller should be implemented in this situation, while traditional PID control algorithm is hard to meet this requirement. Taking constraints on state variables, manipulated variables and output variables into account, model predictive control (MPC) could explicitly optimize closed-loop performance. Then, this method has drawn significant attention of power electronics researchers^[4-5].

A three-phase unity power factor rectifier based on MPC with variable hysteresis current control based on

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three-phase stationary coordinate system is proposed^[6]. Model predictive control is used in marine electric power plant and wind energy conversion systems^[7-8]. A recurrent neural network method for solving convex quadratic program is proposed^[9]. The model predictive control for PWM rectifier based on two-phase rotating dq coordinate system is investigated^[10]. A fixed frequency model predictive control method for three-phase PWM rectifier is proposed^[11-12]. Two weight coefficients are added in cost function^[13-14]. The optimized weight factor of the cost function is essential^[15]. A model predictive direct power control of the three-phase PWM rectifier is proposed^[16-17].

However, the above MPC methods for the rectifier based on finite control set are MPCs without constraints. Due to no constraints, these methods have some shortcomings, such as easy to go out of bounds, large fluctuation of control variables and large output deviation. As a result, it is difficult to guarantee high quality of the power from the grid while current tracking error can't be small as expected. In this paper, constrained MPC is proposed to track mixed signal precisely. Control precision and performance will be improved while duty ratio of each switch has been controlled as a continuous variable in the range of $[0, 1]$.

This paper is organized as follows. In Section 2, the decoupled model and predictive model of the three-phase PWM rectifier under three-phase stationary coordinates system are built. The constrained MPC strategy is proposed to optimize duty ratio of each switch of three-phase PWM rectifier in Section 3. Section 4 introduces simulation and experiment results. Finally, Section 5 concludes this paper.

2 Model of the rectifier

The topology of the three-phase rectifier is shown in Fig. 1. As we can see, there are 6 power switches in the rectifier, which are named as $S_1 - S_6$. e_a, e_b , and e_c are grid voltages, i_a, i_b , and i_c are grid currents.

The filter inductors L transmit energy and R is the equivalent resistor of the inductors L . The output of the rectifier are load resistance R_L in parallel with capacitor C which is equivalent to two capacitor $2C$ in series. V_o is the output DC voltage of the rectifier, and i_o is the output current of the rectifier.

Obviously, three-phase voltages, three-phase currents and control variables couple each other in model of the rectifier. In order to simplify the modelling complexity, it is practical to suppose that the three-phase power grid is working under a balanced condition. Then, it can be separated into three single-phase independent circuits while neutral points N and n are at the same potential.

The circuit illustrated in Fig. 1 was simulated using bipolar sinusoidal pulse width modulation under a

switching frequency of 20 kHz, result in a output DC voltage is 600 V. Fig. 2 shows that the instantaneous voltage between the reference point N and n is not zero and the voltage amplitude is half of the DC voltage. However, the average value of V_{Nn} is zero in a switching period, the reference points N and n of the system are considered at the same potential.

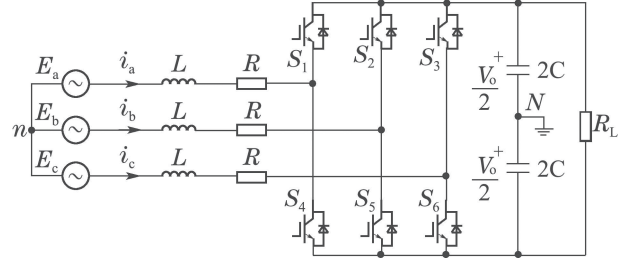


Fig. 1 Topology of the three-phase rectifier

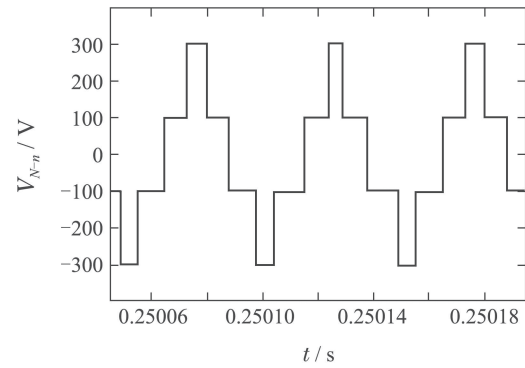


Fig. 2 Instantaneous voltage between the point N and n

The decoupled model of the system are shown in Fig. 3. According to Kirchhoff's law of voltage and current, mathematical model of the rectifier can be described as the following equations.

$$\begin{cases} L \frac{di_a}{dt} = e_a - Ri_a - V_o u_{1a}, \\ L \frac{di_b}{dt} = e_b - Ri_b - V_o u_{1b}, \\ L \frac{di_c}{dt} = e_c - Ri_c - V_o u_{1c}, \end{cases} \quad (1)$$

$$C \frac{dV_o}{dt} = i_a u_a + i_b u_b + i_c u_c - i_o, \quad (2)$$

where $u_a, u_b, u_c \in [0, 1]$ denote duty cycle of switches of the legs, A, B , and C , respectively. $u_{1a} = u_a - \frac{1}{2}$, $u_{1b} = u_b - \frac{1}{2}$, $u_{1c} = u_c - \frac{1}{2}$, $u_{1a}, u_{1b}, u_{1c} \in [-\frac{1}{2}, \frac{1}{2}]$.

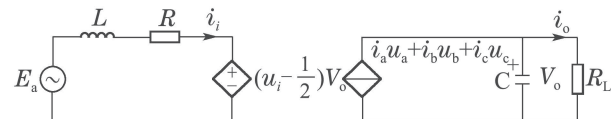


Fig. 3 The decoupled equivalent per-phase circuit of the rectifier N and n

The voltage V_o is a variable value while working under the steady state, but it can be considered as a constant value in one control cycle. Continuous model of the rectifier can be written in state-space form as

$$\frac{di}{dt} = Ai + Bu + Ne_g, \quad (3)$$

where

$$i = (i_a \ i_b \ i_c)^T, \quad u = (u_{1a} \ u_{1b} \ u_{1c})^T, \quad e_g = (e_a \ e_b \ e_c)^T, \\ A = -\frac{R}{L}E, \quad B = -\frac{V_o}{L}E, \quad N = \frac{1}{L}E, \\ E = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix}.$$

Digital implementation of the predictive control requires discrete-time model. The continuous-time model (3) is described as

$$i(k+1) = Gi(k) + Hu(k) + We_g(k), \quad (4)$$

where $G = e^{AT_s}$, $H = \int_0^{T_s} e^{At} B dt$, $W = \int_0^{T_s} e^{At} N dt$, and T_s is the control period.

Prediction horizon P and control horizon M are two essential parameters in MPC. The prediction horizon determines how long will the controller takes to predict the behavior of the rectifier until computing an optimal control. The control horizon determines how long will the controller takes to update prediction of the control variables. They are chosen to get a good trade-off between performance and computation time.

The predictive model of the rectifier can be written as

$$I(k) = \Psi i(k) + \Theta U(k) + \Lambda E_g(k), \quad (5)$$

where

$$I(k) = \begin{bmatrix} i(k+1|k) \\ \vdots \\ i(k+P|k) \end{bmatrix}_{P \times 1}, \quad \Psi = \begin{bmatrix} G \\ \vdots \\ G^P \end{bmatrix}_{P \times 1}, \\ U(k) = \begin{bmatrix} u(k) \\ \vdots \\ u(k+M-1) \end{bmatrix}_{M \times 1}, \\ E_g(k) = \begin{bmatrix} e_g(k) \\ \vdots \\ e_g(k+M-1) \end{bmatrix}_{M \times 1}, \\ \Theta = \begin{bmatrix} H & 0 & s & 0 \\ GH & H & s & 0 \\ \vdots & \vdots & \ddots & \vdots \\ G^{P-1}H & G^{P-2}H & s & G^{P-M}H \end{bmatrix}_{P \times M}, \\ \Lambda = \begin{bmatrix} W & 0 & s & 0 \\ GW & W & s & 0 \\ \vdots & \vdots & \ddots & \vdots \\ G^{P-1}W & G^{P-2}W & s & G^{P-M}W \end{bmatrix}_{P \times M}.$$

3 Predictive control of the rectifier

In this paper, the controller, proposed to guarantee unit power factor and a stable regulated voltage, consists of a fast-inner input-current loop and a outer-voltage loop. The outer-voltage loop computes the reference value of the input current and the inner-current loop is built to achieve fast current tracking between reference input current and real input current.

3.1 Predictive control strategy

MPC is usually used to predict output trajectory of a process and compute a series of control actions, by minimizing the error between the predicted trajectory and desired trajectory. The predictive control strategy is proposed to minimize current tracking errors between reference input current and real input current of the rectifier. The cost function is defined as following:

$$V(k) = [I^*(k) - I(k)]^T Q [I^*(k) - I(k)] + U(k)^T R U(k), \quad (6)$$

where

$$I^*(k) = (i^*(k+1) \cdots i^*(k+P))^T_{P \times 1}, \\ i^*(k+i) = (i_a^*(k+i) \ i_b^*(k+i) \ i_c^*(k+i))^T.$$

Substituting (5) into (6)

$$V(k) = \frac{1}{2}U^T(k)\Phi U(k) + f^T U(k) + m, \quad (7)$$

where

$$\Phi = 2(\Theta^T Q \Theta + R), \quad \Gamma = \Psi i(k) + \Lambda E_g(k), \\ f^T = -2[I^*(k) - \Gamma]^T Q \Theta, \\ m = [I^*(k) - \Gamma]^T Q [I^*(k) - \Gamma].$$

Because m is a constant in one control cycle, it can be ignored to solve quadratic program. The cost function can be obtained in quadratic program form:

$$\begin{cases} \min F(U) = \frac{1}{2}U^T \Phi U + f^T U, \\ \text{s.t. } -\frac{1}{2} \leq U \leq \frac{1}{2}. \end{cases} \quad (8)$$

3.2 Fast optimization algorithm

In this paper, predictive control based on the state-space average model can eventually be transformed into solving the quadratic program (QP) problem. Since the cost function (8) is a convex quadratic program, it is important to choose an appropriate optimization algorithm to guarantee the rapidity of the computation.

3.2.1 Recurrent neural network algorithm

The recurrent neural network (RNN) method is chosen to deal with convex quadratic program is proposed^[8]. The optimization algorithm can guarantee that control variables converge rapidly in the limited time and improve the stability of system.

The Lagrangian function of (8) can be expressed as

$$L(U, \delta, \eta) = \frac{1}{2}U^T \Phi U + f^T U - \delta^T (U - \eta). \quad (9)$$

According the saddle point theorem, the optimal solution of (8) satisfies

$$\begin{cases} \eta = U, \\ U = P_u(\eta - \delta), \\ \Phi U + f - \delta = 0. \end{cases} \quad (10)$$

Equation (10) can be rewritten as:

$$\begin{cases} W_c \delta + l = P_u(W_c + l - \delta), \\ U = W_c \delta + l, \end{cases} \quad (11)$$

where

$$\begin{aligned} W_c &= \Phi^{-1}, \quad l = -\Phi^{-1}f, \\ P_u(x_i) &= [P_u(x_1) \ P_u(x_2) \ \cdots \ P_u(x_m)]^T, \\ & \quad i = 1, 2, \dots, m, \\ P_u(x_i) &= \begin{cases} 0, & x_i < 0, \\ x_i, & 0 \leq x_i \leq 1, \\ 1, & x_i > 1. \end{cases} \end{aligned}$$

The single-layer RNN method is used to solve (10). The dynamic equation is as follows:

$$\begin{cases} \frac{d\delta}{dt} = \theta [P_u(W_c \delta + l - \delta) - W_c \delta - l], \\ U(t) = W_c \delta(t) + l. \end{cases} \quad (12)$$

According (12), we get stable value of δ , predictive controller with single-layer neural network computation is designed to find optimal solution of U .

3.2.2 Stability analysis of RNN

Proof 1 In order to prove that the state trajectory of RNN converges to δ^* , the Lyapunov function is displayed as follow:

$$V = \frac{1}{2} \|G(\delta - \delta^*)\|^2, \quad (13)$$

where the G is a symmetrical positive determined matrix, which gives

$$G^2 = I + W_c.$$

Then derivation of the Lyapunov function can be written as

$$\frac{dV}{dt} = (\delta - \delta^*)^T G^2 \theta \{P_u(W_c \delta + l - \delta) - W_c \delta - l\}.$$

From (10) and (12), we obtain

$$\begin{aligned} & \{P_x(W\lambda + q - \lambda) - W\lambda^* - q\}^T \times \\ & \{(\lambda^* - \lambda) - P_x(W\lambda + q - \lambda) + W\lambda + q\} \geq 0. \end{aligned}$$

Thus, we have

$$\begin{aligned} & (\lambda - \lambda^*)(I + W)\{P_x(W\lambda + q - \lambda) - \\ & W\lambda - q\} \leq -(\lambda - \lambda^*)^T W(\lambda - \lambda^*) - \\ & \|P_x(W\lambda + q - \lambda) - W\lambda - q\|^2 \leq 0. \end{aligned}$$

Therefore, $\frac{dV}{dt} \leq 0$, resulting in the system converges to a stable state.

Proof 2 In order to prove that RNN can converge in a finite time in each sampling time, giving

$$f(\lambda(t)) =$$

$$\|P_x(W\lambda(t) + q - \lambda(t)) - W\lambda(t) - q\|^2, \quad (14)$$

where $\delta(t)$ obtains the state trajectory of (12), thus, we have $f(\delta(t)) \geq 0$, $f(\delta(t_0)) > 0$. Because $f(\delta(t))$ is continuous, there are $\tau > 0$ and $\lambda < 0$ satisfying for arbitrary $t \in [t_0, t + \tau]$, resulting in $f(\delta(t)) \geq \lambda$. According to Proof 1, we obtain

$$\frac{dV}{dt} \leq -\theta \|P_u(W_c \delta + l - \delta) - W_c \delta - l\|^2.$$

Therefore, we have

$$\begin{aligned} & V(\lambda(t)) \leq \\ & V(\lambda(t_0)) - \mu \int_{t_0}^t \|P_x(W\lambda(s) + q - \\ & \lambda(s) - W\lambda(s) - q)\|^2 ds \leq \\ & (V(\lambda(t_0)) - \mu \int_{t_0}^{t_0+\tau} \|P_x(W\lambda(s) + q - \\ & W\lambda(s) - q)\|^2 ds = \\ & V(\lambda(t_0)) - \mu \int_{t_0}^{t_0+\tau} f(\lambda(s)) ds \leq \\ & V(\lambda(t_0)) - \mu \delta \tau, \end{aligned}$$

where $\theta = V(\delta(t_0))/(\tau\lambda)$, if $t \geq t_0 + \tau$, we obtain

$$V(\delta(t)) \leq V(\delta(t_0)) - \theta\lambda\tau = 0.$$

Therefore, as long as the δ could be large enough, the problem (8) can converge in a limited time in each sampling time.

3.2.3 Comparing of optimization algorithms

In order to improve dynamic and stable performance, several algorithms have been proposed to solve the QP problem, such as fast gradient method (FGM)^[18], alternating direction method of multipliers (ADMM)^[19] and RNN. A simulation circuit has been built in Simulink and the three optimal algorithms are programmed by using S-function module, receptivity. At the same time, they are working under the same P and Q parameters. In order to compare the computing speed of three algorithms, tic and toc instructions are added before and after the each small cycle, receptivity, to record the number of single-step small cycles, one-step optimization time and the total time when the system reaches steady state. And the recorded data is shown in Table 1.

Table 1 Comparing the computing speed of optimization algorithms

| Algorithm | FGM | RNN | ADMM |
|--|-------|-------|-------|
| The number of small cycle | 8 | 4 | 26 |
| Maximum time for single-step optimization/ms | 0.112 | 4.085 | 19.53 |
| Minimum time for single-step optimization/ms | 0.033 | 0.058 | 0.137 |
| Total time to steady state/ms | 20.03 | 8.950 | 30.42 |

The number of small cycle and the optimization time of small cycle have the greatest impact on the over-

all online computing time of the algorithm, while small cycles can only be executed serially in the FPGA. Compared with FGM and ADMM, the RNN has the characteristics of the least number of small cycles, the shortest time to reach steady state and the fastest on-line calculation speed. Then, the RNN is choose to implement in this paper.

3.3 Unity power factor rectifier

In order to achieve unity power factor, sinusoidal input currents should be in phase with the line voltages, then $i = ke_g$. In order to linearize the output voltage dynamically, the amplitude control k is conceived. We assume that the reference input current by computing the value is k , and the value of k can be derived as follows:

$$\begin{cases} \varepsilon_a = ke_a - i_a, \\ \varepsilon_b = ke_b - i_b, \\ \varepsilon_c = ke_c - i_c, \end{cases} \quad (15)$$

where $\dot{\varepsilon} = 0$, $\varepsilon = (\varepsilon_a \ \varepsilon_b \ \varepsilon_c)^T$.

Substituting (1) and (2) into (15), we can obtain

$$C \frac{dV_o}{dt} = \frac{k}{V_o} (e_a^2 + e_b^2 + e_c^2) - \frac{kL}{V_o} \frac{dk}{dt} (e_a^2 + e_b^2 + e_c^2) - \frac{k^2 L}{V_o} (e_a \frac{de_a}{dt} + e_b \frac{de_b}{dt} + e_c \frac{de_c}{dt}) - i_o, \quad (16)$$

where the grid voltages have a sinusoidal waveform defined by

$$\begin{cases} e_a = V_s \cos(\omega_0 t), \\ e_b = V_s \cos(\omega_0 t - \frac{2\pi}{3}), \\ e_c = V_s \cos(\omega_0 t - \frac{4\pi}{3}). \end{cases} \quad (17)$$

Substituting (17) in (16), the following equation is deduced.

$$C \frac{dV_o}{dt} = \frac{3V_s^2 k}{2V_o} - i_o. \quad (18)$$

In order to linearize the output voltage dynamically, the nonlinear control of k is proposed as follows:

$$k = \frac{2V_o}{3V_s^2} [k_i \int_{-\infty}^t (V_o^* - V_o) d\tau + k_p (V_o^* - V_o) + \frac{V_o}{R_L}]. \quad (19)$$

The calculation of the parameter k is obtained by PI control with feedforward control of the output voltage which can provide a regulated output DC-voltage with fast dynamics response against sudden changes in the load.

4 Simulation and experiment

In this section, effectiveness of the proposed constrained MPC strategy is verified by simulation in MATLAB/Simulink and experiment of rectifier. The parameters used in the simulation and experiment are given in Table 2.

4.1 Simulation

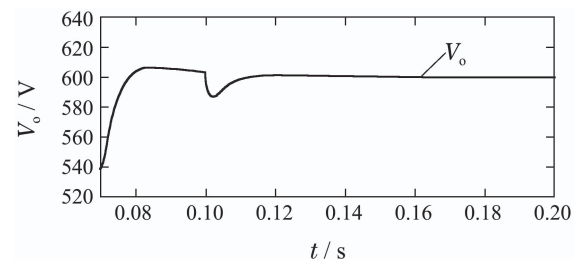
In order to verify the performance of the proposed constrained MPC method, a predictive controller was implemented of the three-phase PWM rectifier by MATLAB/Simulink based on the parameters given in Table 2. S -Function module is designed according to MPC to achieve the function of the predictive controller, and the circuit module and controller module form the entire simulation system.

Table 2 Parameters of the rectifier

| Parameters | Description | Value |
|------------|---------------------|--------------|
| V_o | Output DC voltage | 600 V |
| P_e | Rated power | 10 kW |
| V_g | Grid RMS voltage | 220 V |
| f_n | Grid frequency | 50 Hz |
| T_s | Control period | 50 μ s |
| L | Filter inductance | 3 mH |
| R | Equivalent resistor | 0.1 Ω |
| C | Filter capacitance | 1000 μ F |
| R_L | Load resistor | 36 Ω |

Prediction horizon and control horizon directly affect not only the amount of calculation, but also rapidity and stability of the rectifier. A MPC with smaller P and larger M has greater rapidity and worse stability. On the contrary, larger P and smaller M will cause worse rapidity and greater stability. In this paper, P and M are set as 2 to gain good tradeoff between rapidity and stability. The weighting factors q and r are closely related to the tracing error and power quality. The guideline for weighting factors design proposed in [12] is used to select weighting factors. The weighting factors q and r is set as 6000 and 0.1, respectively, to gain good trade-off and power quality. The simulation results of constrained MPC and PI controller are shown in Fig. 4 and Fig. 5.

In the simulations, the rectifier operated at rated power, and the reference DC voltage was 600 V. As shown in Fig. 4 and Fig. 5, the output DC voltage is 600 V with small steady-state error and the input current of each phase is almost 15 A. The input current keeps the same phase with the grid voltage, then the rectifier unit power factor operation function is achieved. But there are significant differences between two methods.



(a) Output DC voltage

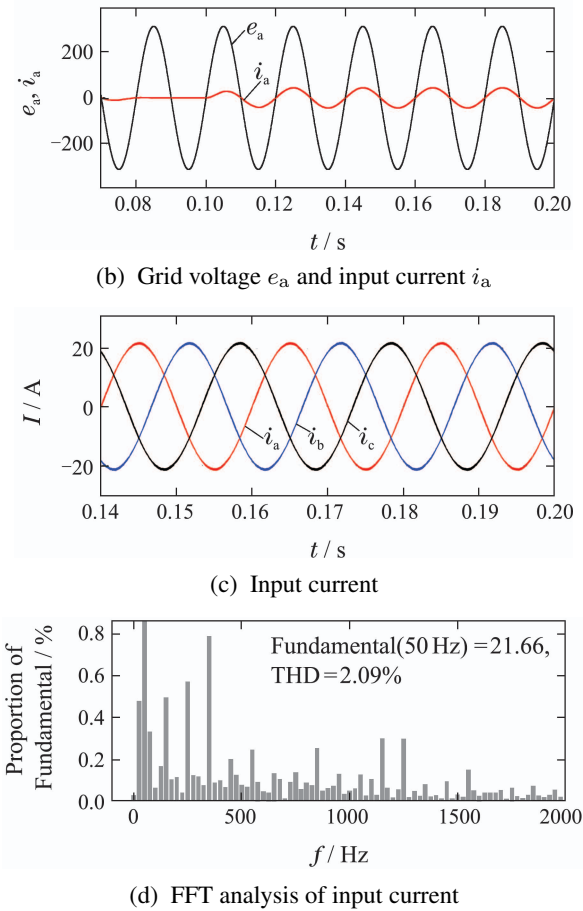


Fig. 4 Simulation results of constrained MPC

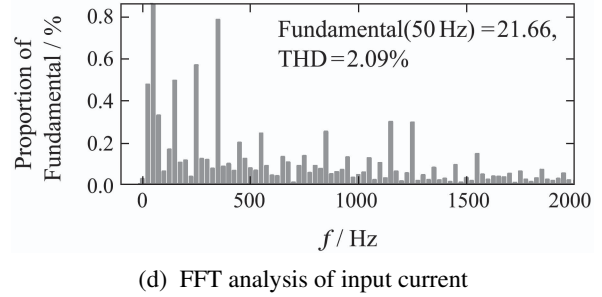
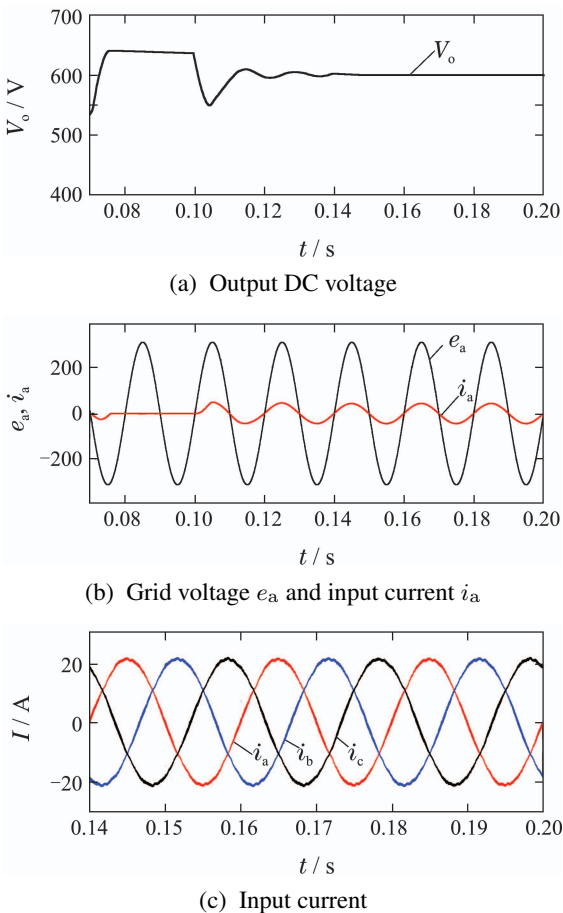


Fig. 5 Simulation results of PI controller

Compared with PI controller, input current of constrained MPC is obviously smoother. What's more, constrained MPC reduces total harmonics distortion (THD) by 1.15% compared with PI controller. Hence, the constrained MPC strategy has a good performance in harmonics characteristic.

To get better comparison of dynamic performance of constrained MPC and PI, we changed reference D-C voltage in operation, and the output DC voltage is shown in Fig. 6 and Fig. 7. After changing the reference value, the time to reach steady state again of constrained MPC is 5 ms, which is much shorter than that of PI controller, about 25 ms. It demonstrates that the constrained MPC has better dynamic performance than PI controller.

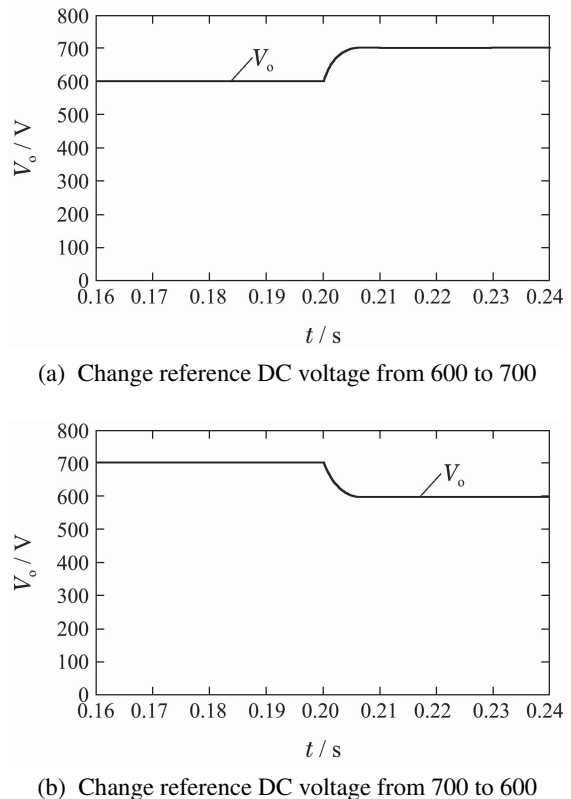
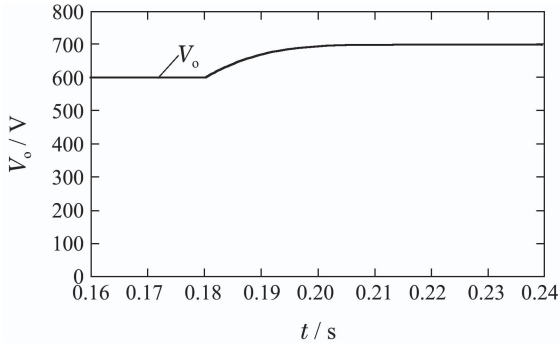
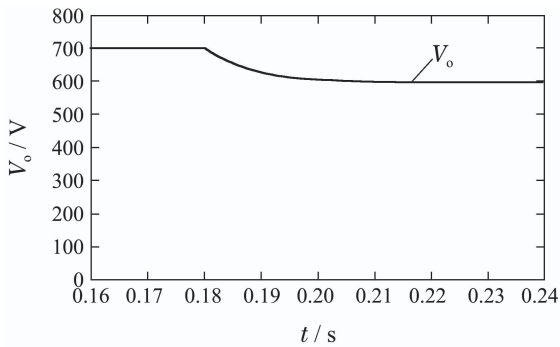


Fig. 6 Output DC voltage of constrained MPC



(a) Change reference DC voltage from 600 to 700



(b) Change reference DC voltage from 700 to 600

Fig. 7 Output DC voltage of PI controller

In conclusion, the constrained MPC strategy has lower THD and faster response speed.

4.2 Experiment

To further validate the feasibility of constrained MPC strategy, a three-phase PWM rectifier was setup according to Fig. 1 and Table 1. Infineon IGBTs were employed. FPGA with the ability of high-speed parallel calculation, is chosen as the controller to implement the real-time control algorithm. As shown in Fig. 8, the structure of FPGA controller consists of Read/Write Module, Parallel Computing Module and I/O ports. Time Control Module is built to control the whole state switches which are described as the state machine. And the MAC module is used to calculate the matrix value from the RAM and ROM. A sampling board with three-channel AC voltage sampling, three-channel AC current sampling and single-channel DC voltage sampling was connected the FPGA control board. Three driver modules were used to receive the PWM signals from FPGA, and drive the IGBTs. The experimental setups of rectifier is shown in Fig. 9.

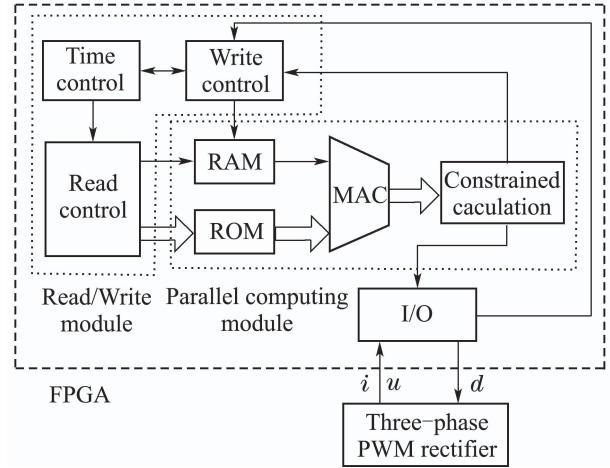


Fig. 8 The structure of FPGA controller

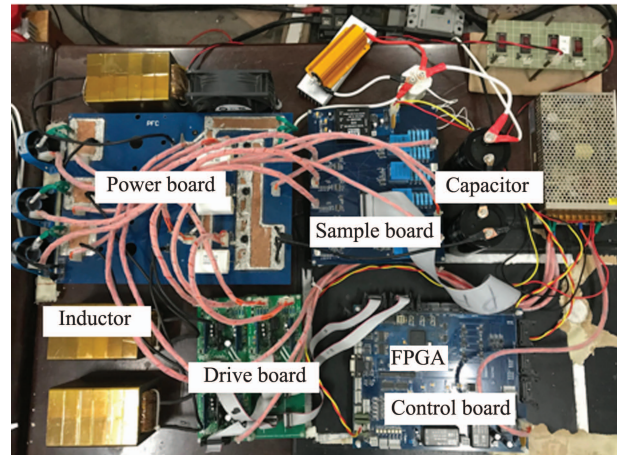
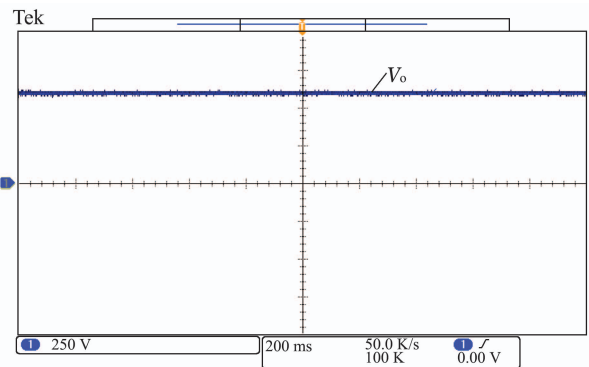
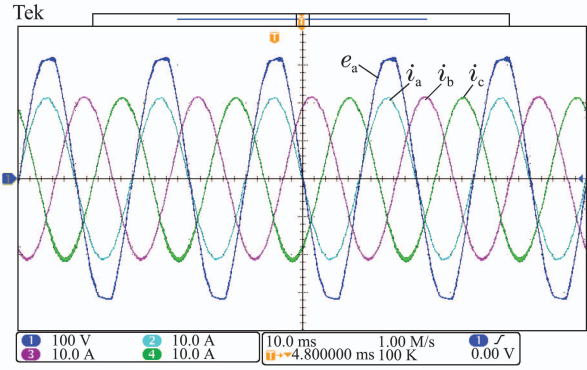


Fig. 9 Photographs of experimental setups of rectifier

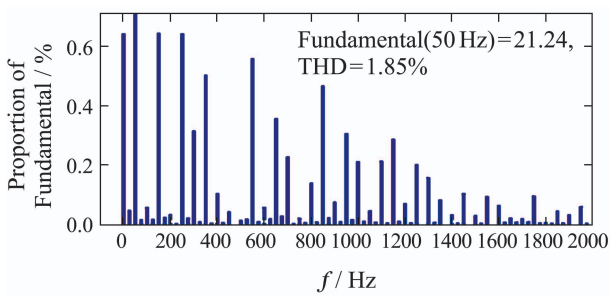
Figures 10–13 are the experimental results of two strategies, respectively. The experimental result of constrained MPC and PI control are very similar to simulation results. The output DC voltage is 600 V with small steady-state error and the sinusoidal input current and the grid voltage of phase A are strictly in the same frequency phase.



(a) Output DC voltage

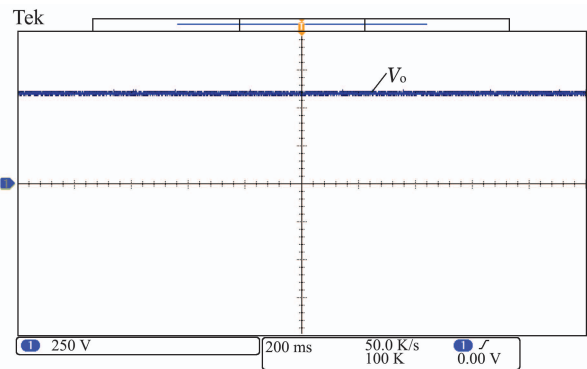


(b) Grid voltage e_a and input current

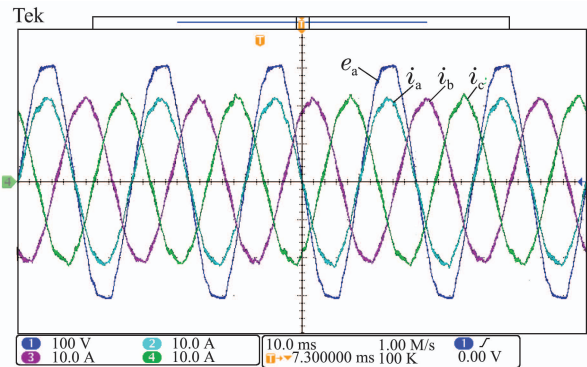


(c) FFT analysis of input current

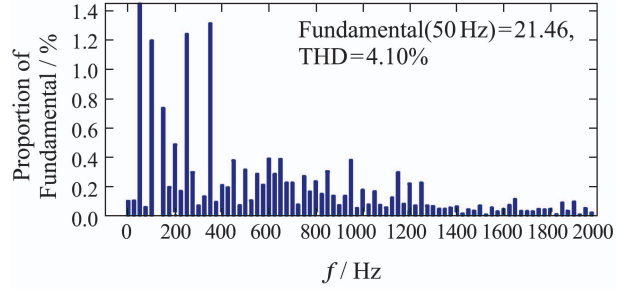
Fig. 10 Experimental results of constrained MPC



(a) Output DC voltage

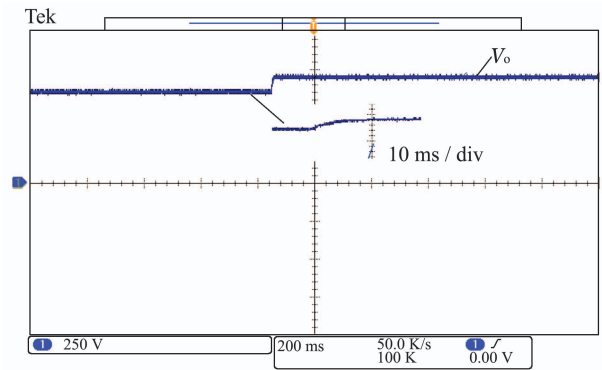


(b) Grid voltage e_a and input current

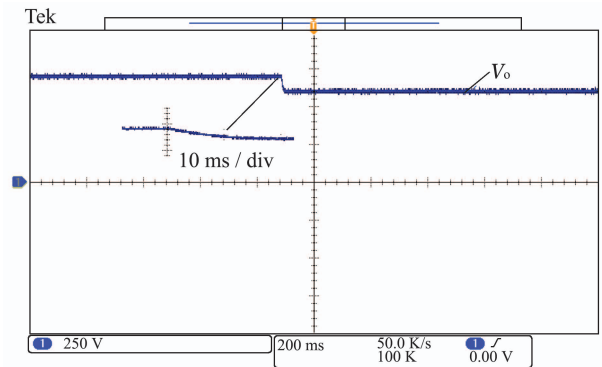


(c) FFT analysis of input current

Fig. 11 Experimental results of PI controller

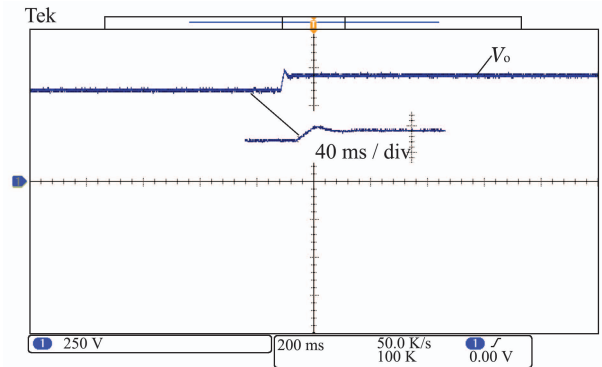


(a) Change reference DC voltage from 600 to 700

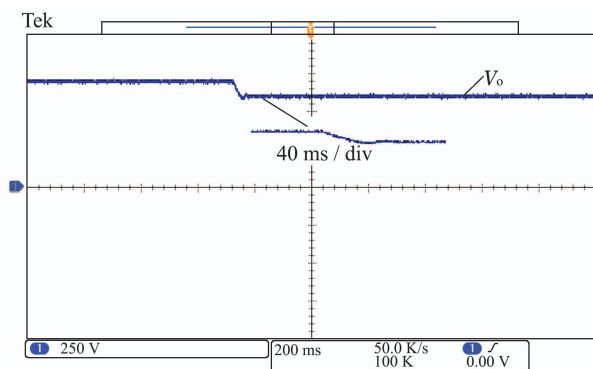


(b) Change reference DC voltage from 700 to 600

Fig. 12 Output DC voltage of constrained MPC



(a) Change reference DC voltage from 600 to 700



(b) Change reference DC voltage from 700 to 600

Fig. 13 Output DC voltage of PI controller

Compared with PI control, constrained MPC strategy decreases THD by 2.25%. After changing the reference value, the time to reach steady state again of constrained MPC is 12 ms, which is much shorter than that of PI controller, about 56 ms. The experimental results have demonstrated that the proposed constrained MPC strategy is feasible and achieves low THD and fast dynamic response.

5 Conclusions

In this paper, a constrained model predictive control strategy has been proposed for a three-phase PWM rectifier. Firstly, a decoupled model of the rectifier is built with no coordinate transformation and the discrete-time state-space equation is deduced. Then, the constrained MPC strategy is investigated and output-voltage loop control method is designed. Finally, simulation and experiment results verify effectiveness and feasibility of the constrained MPC strategy. According to the comparison between constrained MPC and PI control, it is found that constrained MPC can significantly improve steady state and dynamic performance of the rectifier. The measured THD is 2.5% lower and the time to reach steady state is 12 ms, which is one fifth of PI control.

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